

## **REMARKS**

Applicants appreciate the Examiner's allowance of Claims 12-15 in the above-identified application. Applicants will now address each of the Examiner's objections and rejections in the order in which they appear in the Office Action.

### **Drawings**

The Examiner objects to the drawings under 37 CFR §1.83(a). In particular, the Examiner states that the feature of the gate electrode having a first conductive layer and a second conductive layer of the p-channel TFT of the pixel portion having the same width, as recited in Claims 1, 16 and 18, must be shown or the feature canceled from the claims.

In order to advance the prosecution of this application, Applicants have amended Claims 1, 16 and 18 to recite that an upper surface of the first conductive layer and a lower surface of the second conductive layer have the same width. This feature is shown, for example, in Fig. 3B of the present application.

Accordingly, it is respectfully submitted that this objection has been overcome.

### **Claim Rejections – 35 USC §112**

The Examiner also rejects Claims 1-11 and 16-19 under 35 USC §112, first paragraph, for the same reason discussed above for the drawings. For the same reason discussed above, amended independent Claims 1, 16 and 18 clearly overcome this rejection. Accordingly, it is requested that the rejection now be withdrawn.

Conclusion

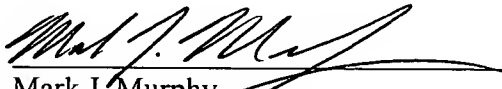
It is respectfully submitted that the present application is in a condition for allowance and should be allowed.

If any fee is due for this amendment, please charge our deposit account 50/1039.

Favorable reconsideration is earnestly solicited.

Respectfully submitted,

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